

## **PRESS RELEASE**

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## **VMEbus Telephony Board Combines Four T1/E1 Interfaces with 4800 MIPS of DSP Processing**

### **Packs 48 TMS C549 DSPs on a Single VMEbus Card**

West Chester, PA. July 19, 1999--Communication Automation & Control (CAC) announced today a new VMEbus telephony board that combines four T1/E1 interfaces with up to 4800 MIPS of digital signal processing power. The new board, dubbed QuicKit/T1, features 48 100-MIPS TMS320VC549 DSPs, which enable the board to provide up to 50 MMACS (million multiply-accumulates per second) of processing power per T1 or E1 time slot.

"QuicKit's four T1/E1 interfaces, high-bandwidth and high-performance PCI, TDM, and SCSA buses make it ideal for moving large amounts of telephony data to and from WANs and the VMEbus," said Jim Bridges, president of CAC. "Equally important, the large number of independent DSPs provides a great deal more usable DSP horsepower for multi-channel applications than alternative approaches that use fewer processors. For example, competitive boards equipped with four TMS320C6201s provide at most 1600 MMACS, while the QuicKit/T1 provides up to 4,800 MMACS."

QuicKit/T1's 48 DSPs are implemented as four PCI modules, each containing 12 DSPs. The board's T1/E1 WAN interface is implemented as a pair of PCI modules, each containing two T1 or E1 interfaces. All six modules are linked via a local PCI bus, which provides access to up to 64 Mbytes of global DRAM. Eight internal TDM (Time Division Multiplexed) buses, optimized for carrying telephony traffic, are used to move data to and from the T1/E1 and DSP modules.

Within each DSP module, the 12 DSPs are configured as two blocks of six DSPs each. Both blocks are fed by two common TDM buses. In addition, the DSPs within each block are connected via their Host-Port Interfaces to the global PCI bus, which is used for DSP control and messaging and for DSP access to global DRAM. Each DSP is equipped with 32 kwords of on-chip memory. In addition, one DSP in each group has access to 256 kwords of external SRAM, which can be used for program or data storage.

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To support larger configurations comprising multiple QuicKit/T1 boards, and to facilitate communications with other VMEbus telephony boards, QuicKit provides an optional SCSA (Signal Computing System Architecture) bus interface. Through the SCSA bus, which is implemented using the VMEbus P2 connector, multiple telephony boards can communicate over the VMEbus backplane without interfering with system bus traffic.

### **Pricing and Availability**

Single unit cost for a QuicKit/T1 board equipped with four T1/E1 interfaces, 32 Mbytes of global DRAM, and 48 DSPs is \$17,400. Deliveries run 6 to 8 weeks ARO.

### **About Communication Automation & Control, Inc.**

Headquartered in West Chester, PA, Communication Automation & Control designs, manufactures and markets DSP and RISC processor products for data acquisition, signal processing, and configurable computing. CAC's single- and multiprocessor board-level products utilize DSPs from Texas Instruments and Lucent, and are available for a variety of form factors, including ISA, PCI, VME and PCMCIA.

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